

Road Opening Assessment

- The Incident Controller (IC) is responsible for deciding when and where traffic management will be installed, or closing a road, and returning the road to the asset owner for inspection and reopening.
- Under the Traffic Management Plan (TMP) is the responsibility of the Plans Unit within the Planning Section with cooperation from the Operations Section to work with the Incident Controller (IC) to determine if a road can be reopened. Main Roads WA or a Local Government (LG) may help with this and the Operations Section must work closely with Planning to develop and revise the TMP accordingly.
- The Planning Officer is responsible for preparing forms for approval by the IC to change the status of roads and road closures.
- Roads that are closed will remain closed until such time as a risk assessment by the IC has been completed and an inspection and remedial action can be undertaken. *See relevant Hazard Management Agency or Controlling Agency Risk Assessment documentation.*
- The decision to change the status of a road closure must be associated with a risk assessment and be authorised by the IC.
- The first stage of the process for reopening of roads can only be authorised by the IC, with the road then returned to the asset owner.
- Hazard Management Agency or Controlling Agency must conduct a full safety survey of the road. Inspection of the fire ground edge, mopping-up and treatment of hazardous trees should be undertaken in daylight hours where possible.
- I C must complete a risk assessment, in conjunction with the asset owner or a network operator. *Please use the 'Road Opening Checklist' prior to re-opening the road.*
- I C must determine that the road can be safely reopened. Asset owner or network operator conducts a full safety survey of the road, if necessary, in company with a Hazard Management Agency or Controlling Agency representative.
- Incident Controller formally returns the road to the asset owner or network operator. The asset owner or network operator formally accepts control from the IC.